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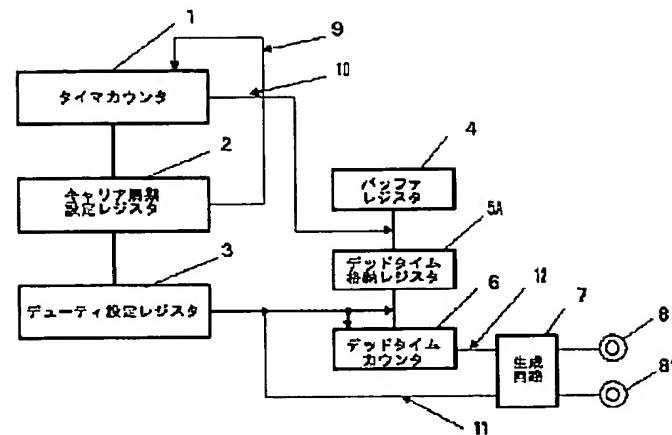
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APPLICANT : NEC CORP;

INVENTOR : BABA TAIJI;

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TITLE : MICROCOMPUTER FOR INVERTER  
 CONTROL AND INVERTER  
 CONTROLLER



ABSTRACT : PROBLEM TO BE SOLVED: To control output characteristics as desired by constitution capable of easily lowering a price without the need of a high-speed and high-performance CPU or the memory of a large capacity.

SOLUTION: A timer counter 1 repeats up/down counting between '0' and the set value of a carrier cycle setting register 2. By underflow signals 10 when a counted value becomes '0', the set value of a buffer register 4 is loaded to a dead time storage register 5A. When the counted value of the timer counter 1 coincides with the set value of a duty setting register 3, duty coincident signals 11 are outputted and the installation value of the dead time storage register 5A is reloaded to a dead time counter 6. The dead time counter 6 outputs dead time signals 12 at the time of completing the counting. A generation circuit 7 generates the pulse width modulation signals of two systems in which an active level does not overlap based on the duty coincident signals 11 and the dead time signals 12.

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